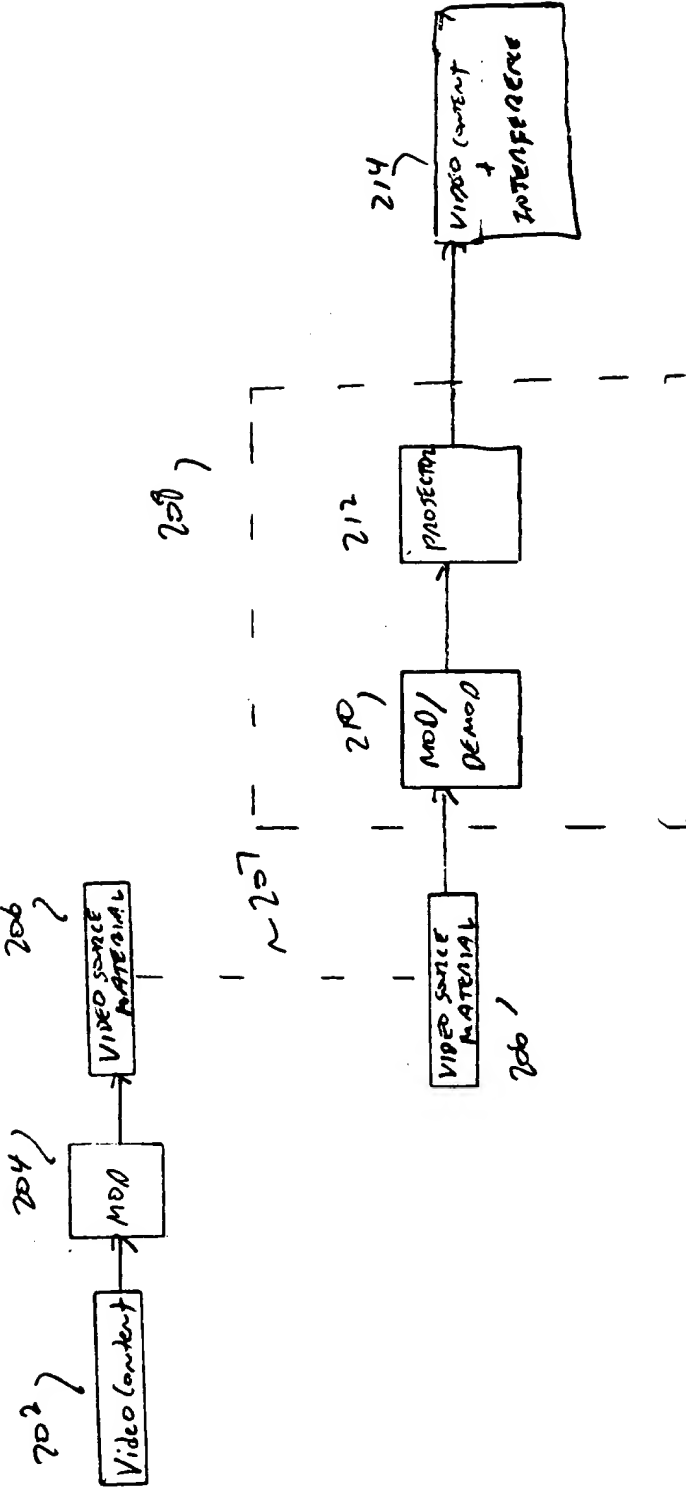


FIG. 2

FIG. 2 is a block diagram of a video processing system.

202



3A

F16

301

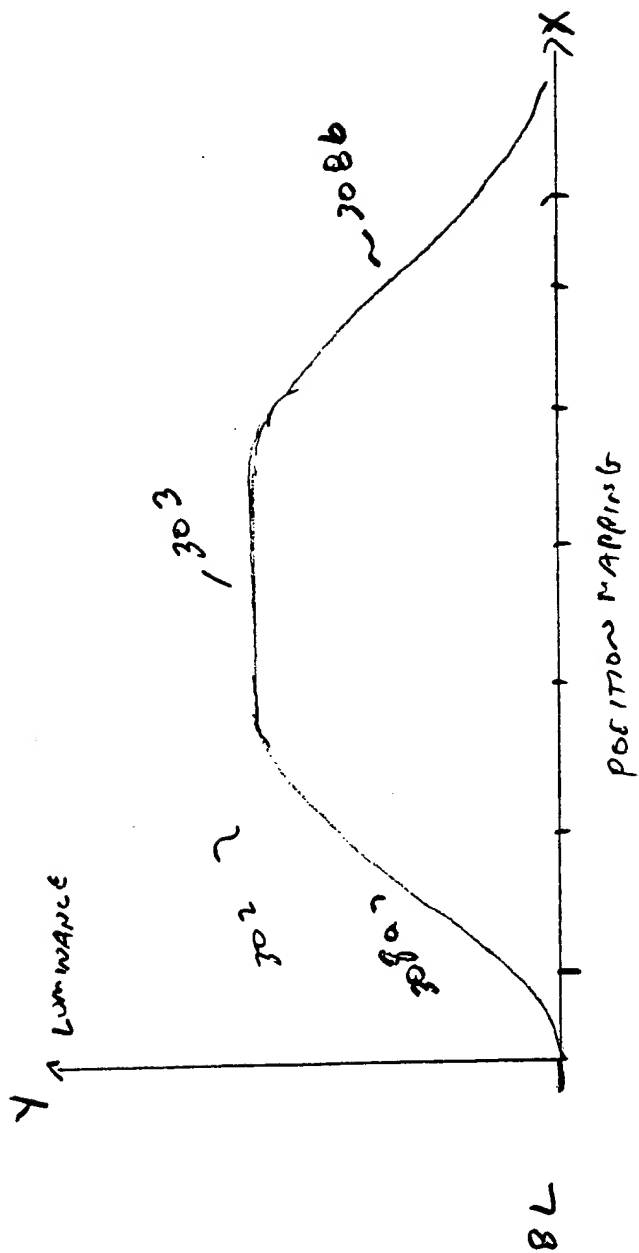
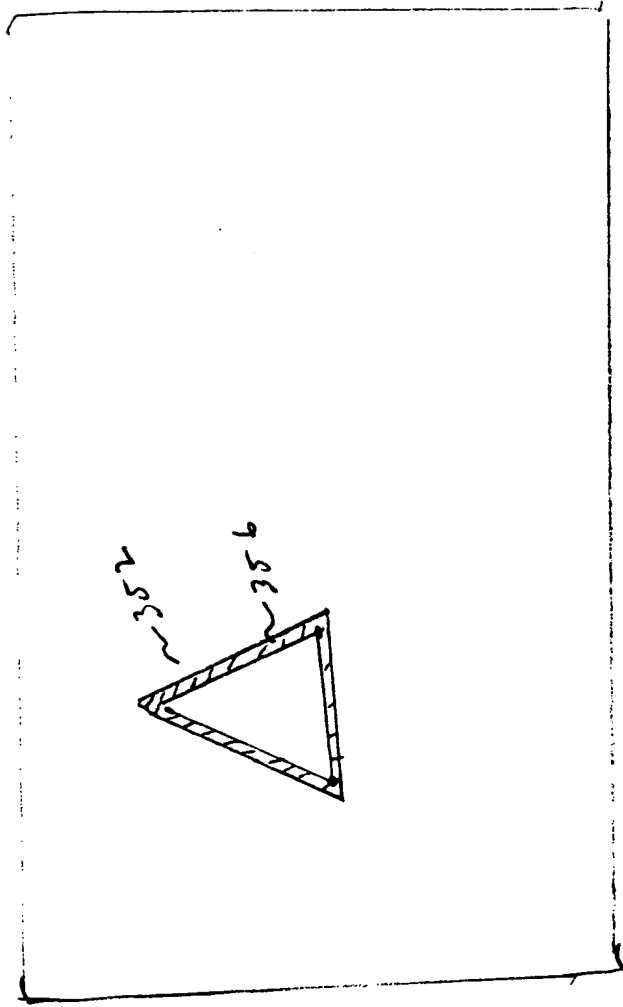


Fig. 3B

350

FIG. 3B



716.4

400

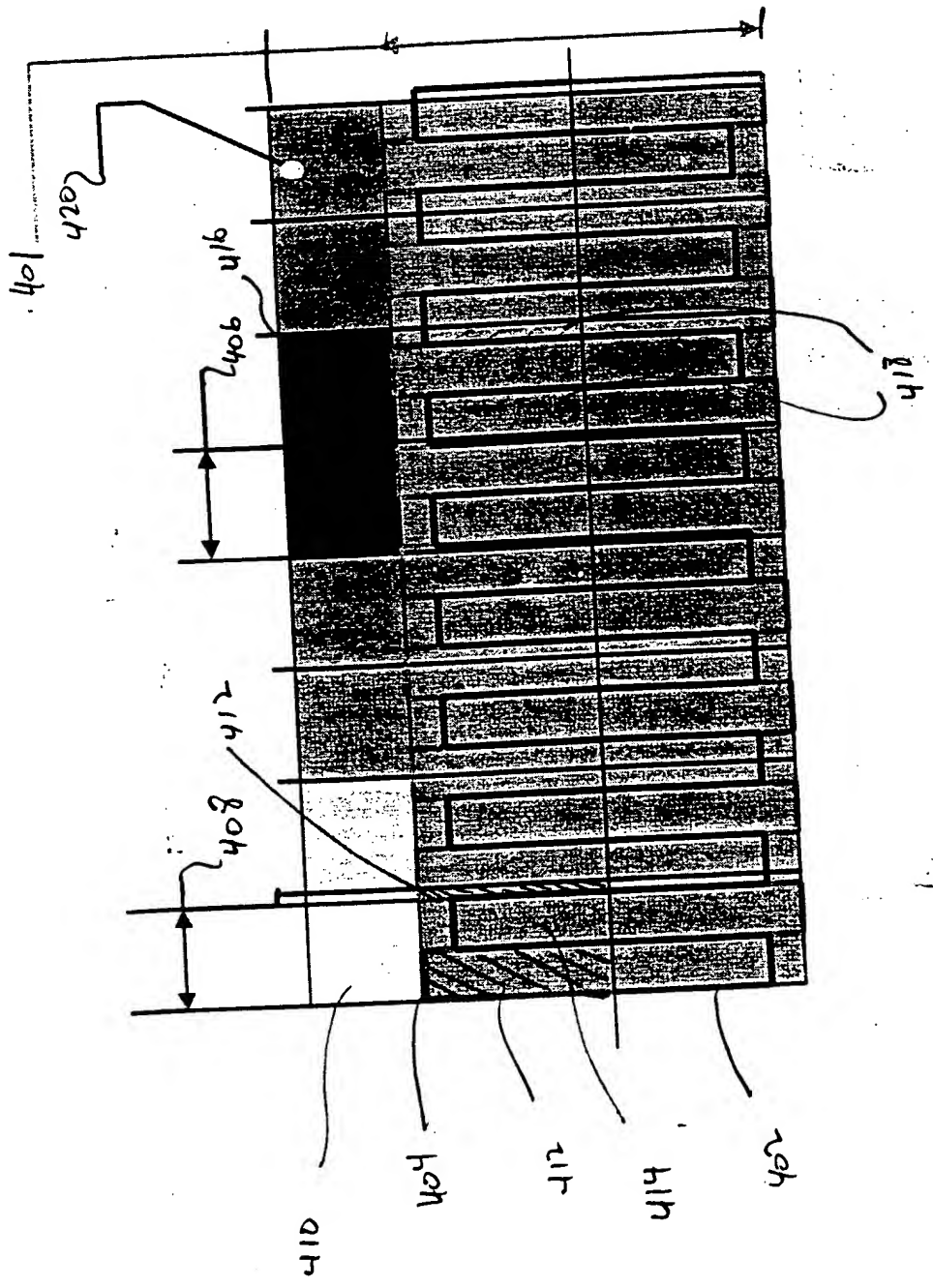
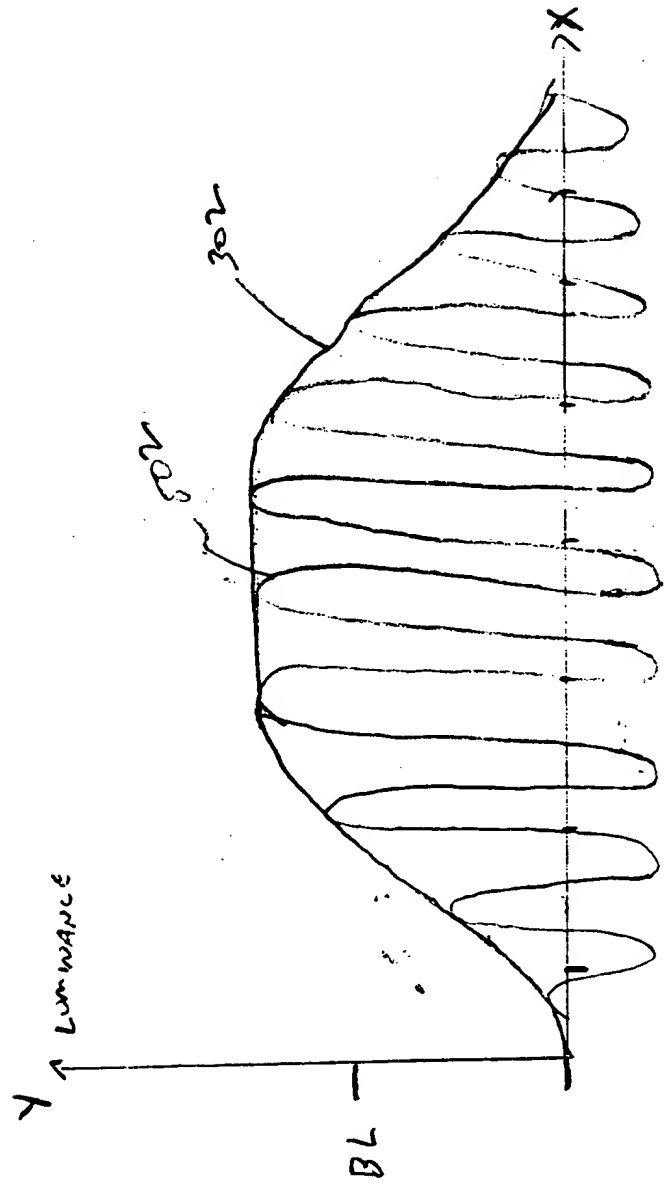


FIG 5

50

FIG 5 shows a cross-sectional view of a device in accordance with the present invention. The device includes a substrate 10, a layer 20, and a layer 30. The layer 20 is disposed on the substrate 10, and the layer 30 is disposed on the layer 20. The layer 30 includes a series of protrusions 32. The protrusions 32 are disposed in a row, and each protrusion 32 has a rounded top and a flat base. The protrusions 32 are separated by a distance 34. The distance 34 is the distance between the centers of two adjacent protrusions 32. The distance 34 is greater than the width of each protrusion 32. The layer 30 is disposed on the layer 20, and the layer 20 is disposed on the substrate 10. The substrate 10 is a rectangular block, and the layer 20 is a thin layer. The layer 30 is a thin layer, and the protrusions 32 are small protrusions. The device is a cross-sectional view of a device in accordance with the present invention. The device includes a substrate 10, a layer 20, and a layer 30. The layer 20 is disposed on the substrate 10, and the layer 30 is disposed on the layer 20. The layer 30 includes a series of protrusions 32. The protrusions 32 are disposed in a row, and each protrusion 32 has a rounded top and a flat base. The protrusions 32 are separated by a distance 34. The distance 34 is the distance between the centers of two adjacent protrusions 32. The distance 34 is greater than the width of each protrusion 32. The layer 30 is disposed on the layer 20, and the layer 20 is disposed on the substrate 10. The substrate 10 is a rectangular block, and the layer 20 is a thin layer. The layer 30 is a thin layer, and the protrusions 32 are small protrusions.



Chromatic Modulation for time interval Modulators

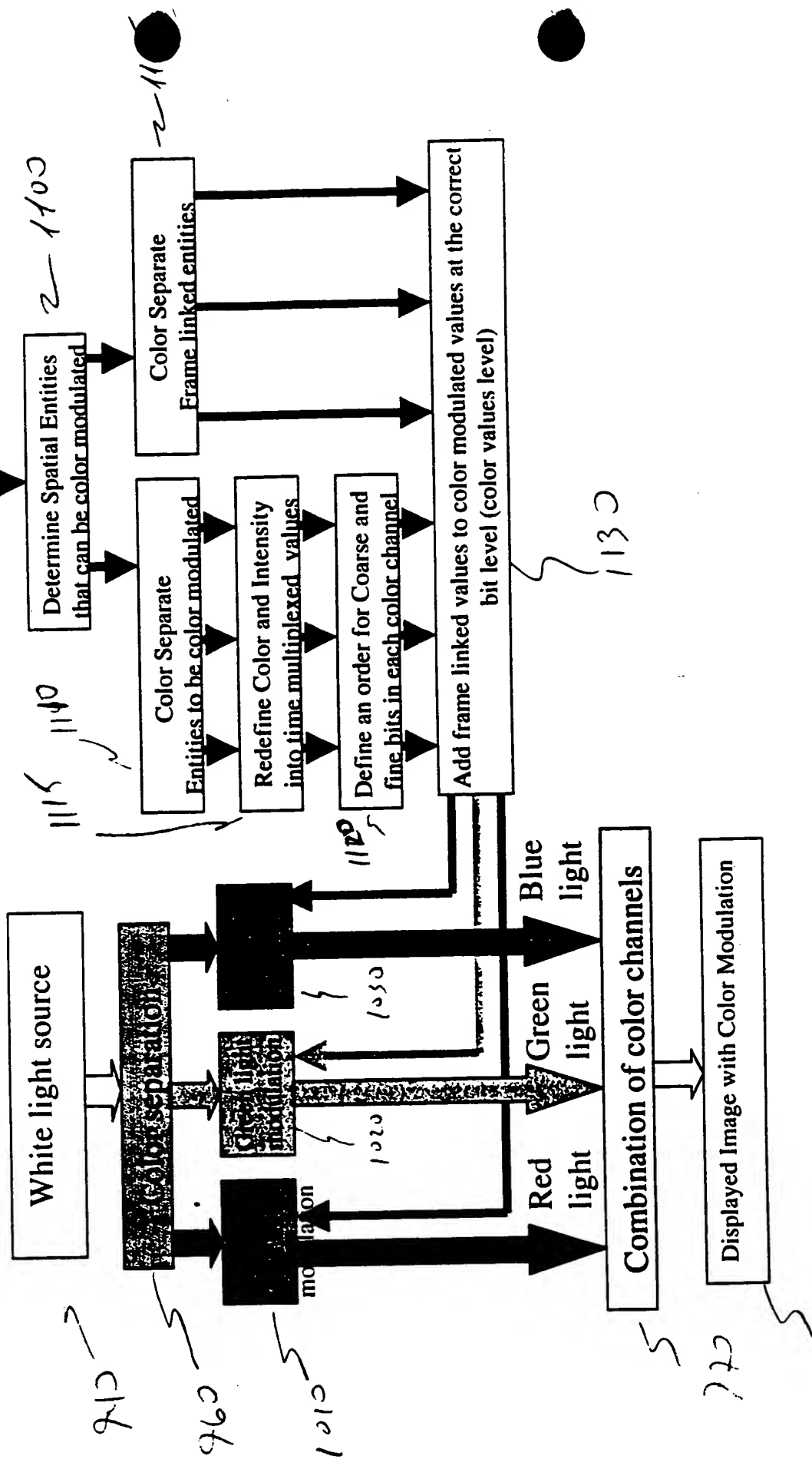


Figure 2-36

FIG. 7

700

702

